

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
PONDICHERRY ENGINEERING COLLEGE**

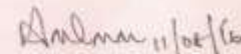
**Minutes of the Study Club meeting held at the Faculty Coordinator's room
(Room no. EC 103) on 3rd August 2016 at 12:40 PM**

The following conclusions were drawn from the meeting:

- The Study club activities for the sophomores (2nd B.Tech) are planned to be commenced from 18th August 2016 (Thursday).
- The Club activities include GRE, GATE and CAT coaching classes. It is planned to be scheduled during last two periods of Tuesday and Thursday of every week.
- The Schedule for the Study club activities will be as follows:
 - The activities will commence with an introduction to all the three categories of competitive Exams.
 - It's planned to have tutorial classes/ illustrative sessions and as well mock test in the alternate weeks which will be followed by a feedback session to benchmark the students' performance.
 - In case of any holidays or regular academic tests, the sessions will be conducted on the following Tuesday and Thursday.
 - The syllabi pertaining to each of the study group along with some useful materials is planned to be hosted in our department web page for providing necessary information to the students.
 - Mr. D. Praveen Kumar, Mr. Z. Ashwin and Mr. S.J.R.Sainiklesh of final year B.Tech are selected as the Student Coordinators for the Management, GRE and Gate club respectively. Mr. D.Praveen Kumar is additionally given the role of a Treasurer.
 - As the Pre-final students do not have a free overlapping slot to be trained by the final years, it has been decided to provide the Question papers for their self-assessment.
- The Student Coordinators are asked to collect a sum of Rs. 100/- as an enrolment fee for the study club from the second year students alone.


11/8/16

Dr K. Jayanthi
(Faculty Coordinator)


11/08/16

Dr. M.Tamilarasi
(Head of the Department)

Copy to:

1. The ECE office
2. The Study club- faculty Coordinator
3. The Final year B.Tech CR /ACR – for circulation.

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
PONDICHERRY ENGINEERING COLLEGE
PUDUCHERRY- 605 014.**

No. PEC/ECE/Students-Council/Study-Forum/2016/

Date: 11-08-2016

Submitted to the Head of the ECE Department

Sir,

As a part of **Students Council** of Department of Electronics and Communication Engineering, Pondicherry Engineering College, the **Study Forum** had planned to resume its active operation for the academic year 2016-2017 from **18th August 2016**. The list of clubs in the Study Forum and their representatives are as follows:

a) GATE CLUB

1. Mr. S.J.R.Sainiklesh , Final Year B.Tech ECE (Student Coordinator)
2. Ms.Valluru Sahethri, Second Year B.Tech ECE (Student Representative)

b) CAT CLUB

1. Mr. D.Praveen Kumar, Final Year B.Tech ECE (Coordinator & Treasurer)
2. Mr. R. Sadeesh Kumar, Second Year B.Tech ECE (Student Representative)

c) GRE CLUB

1. Mr. Z. Ashwin Final Year B.Tech ECE (Coordinator)
2. Ms. P. Madhumitha, Second Year B.Tech ECE (Student Representative)

This is for your kind information and records.



Dr. K. Jayanthi

Co-ordinator/Study Club Forum

Copy to:

4. The ECE office
5. The Study club- faculty Coordinator
6. The Final year CR /ACR – for circulation.

General Notice board.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

PONDICHERRY ENGINEERING COLLEGE

Action plan for the GRE study club (Phase 1)

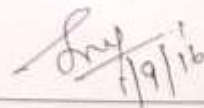
[2016 - 17]

Schedule	Syllabus
Sept - 1 st week	Simple Interest & Compound Interest
Sept 2 nd week	Reading Comprehension
Sept - 3 rd week	Permutations & Combinations
Sept. 4 th week	Text Completion
Oct. 5 th week	Probability & Geometry



11/3/16

Z. Ashwin
(Student Coordinator)



11/9/16

Dr. K. Jayanthi
(Faculty Coordinator)

PONDICHERRY ENGINEERING COLLEGE
DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING
ECE STUDY CLUB
GATE SYLLABUS

NETWORK THEOREMS AND CIRCUIT THEORY

Network solution methods: nodal and mesh analysis; Network theorems: superposition, Thevenin and Norton's, maximum power transfer; Wye-Delta transformation; Steady state sinusoidal analysis using phasors; Time domain analysis of simple linear circuits; Solution of network equations using Laplace transform; Frequency domain analysis of RLC circuits; Linear 2-port network parameters: driving point and transfer functions; State equations for networks.

ELECTROMAGNETICS

Electrostatics; Maxwell's equations: differential and integral forms and their interpretation, boundary conditions, wave equation, Poynting vector; Plane waves and properties: reflection and refraction, polarization, phase and group velocity, propagation through various media, skin depth

ELECTRONIC DEVICES AND CIRCUITS

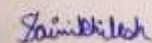
Energy bands in intrinsic and extrinsic silicon; Carrier transport: diffusion current, drift current, mobility and resistivity; Generation and recombination of carriers; Poisson and continuity equations; P-N junction, Zener diode, BJT, MOS capacitor, MOSFET, LED, photo diode and solar cell; Integrated circuit fabrication process: oxidation, diffusion, ion implantation, photolithography and twin-tub CMOS process.


DIGITAL CIRCUITS

Number systems; Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates, arithmetic circuits, code converters, multiplexers, decoders and PLAs; Sequential circuits: latches and flip-flops, counters, shift-registers and finite state machines

ANALOG COMMUNICATIONS

Amplitude modulation and demodulation, angle modulation and demodulation, spectra of AM and FM, superheterodyne receivers, circuits for analog communications;


Student Coordinator
(SJR Sai Nikhilesh)


Faculty Coordinator
(Dr. K Jayanthi)